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## PATENT ABSTRACTS OF JAPAN

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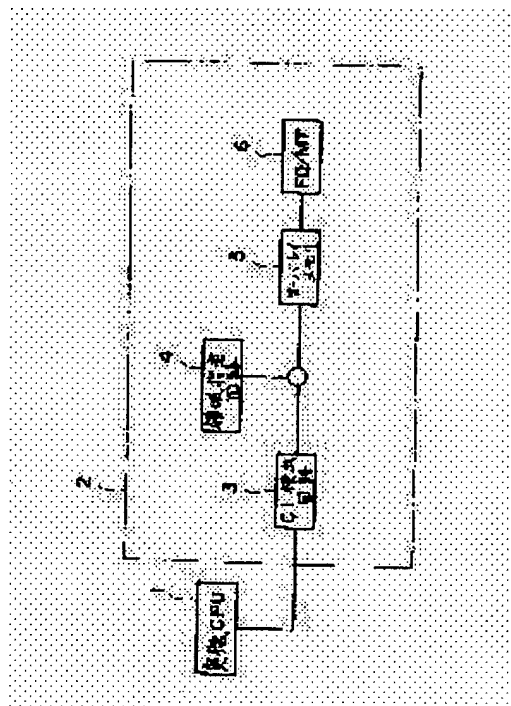
## (54) PROGRAM ANALYZER

## (57)Abstract:

PURPOSE: To collect C1 coverage information on an actual machine and to compress and record the information by providing a circuit which detects the C1 coverage information, area specifying circuit, etc.

CONSTITUTION: A C1 detecting circuit 3 detects whether the instruction being executed by the actual machine CPU1 is a branch instruction or not and whether a branch (jump) is caused by the branch instruction or not, and outputs data such as the address of the branch instruction, etc. An overlay memory 5 operates as a buffer which stores the output temporarily and outputs it at the timing of a recording device 6.

Consequently, even when a device 6 slow in processing time is used, coverage C1 information is collated without decreasing the operation speed of the actual machine CPU1. Further, the area specifying circuit 4 limits an address range of C1 tracing, and consequently data to be recorded is compressed and recorded.



## LEGAL STATUS

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